

FIG. 1 (PRIOR ART)

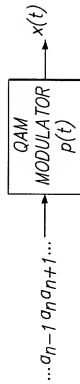


FIG. 2 (PRIOR ART)

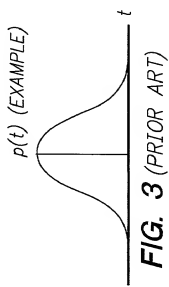


FIG. 3 (PRIOR ART)

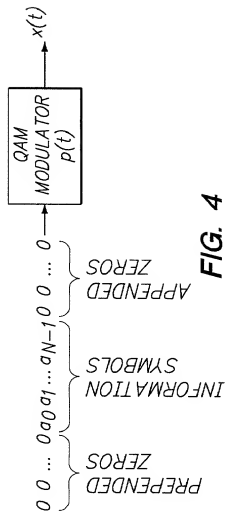


FIG. 4

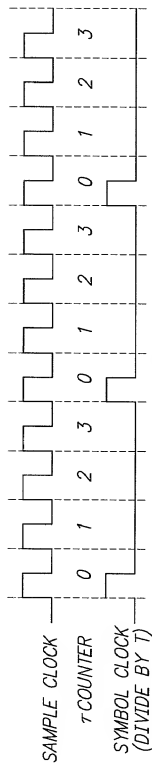


FIG. 5

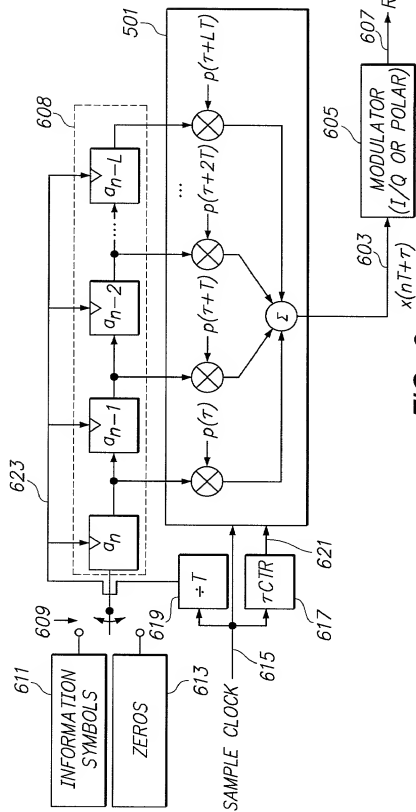
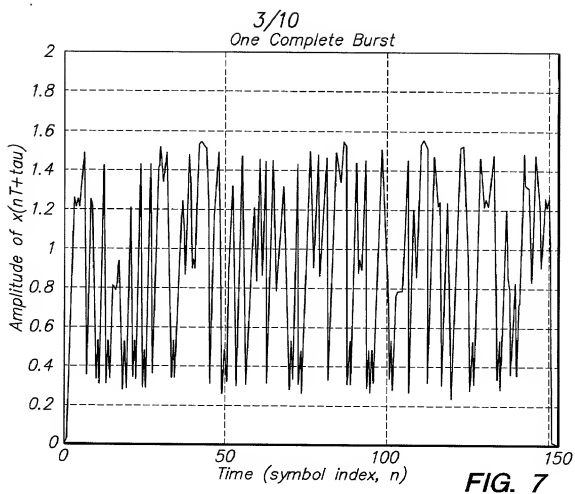
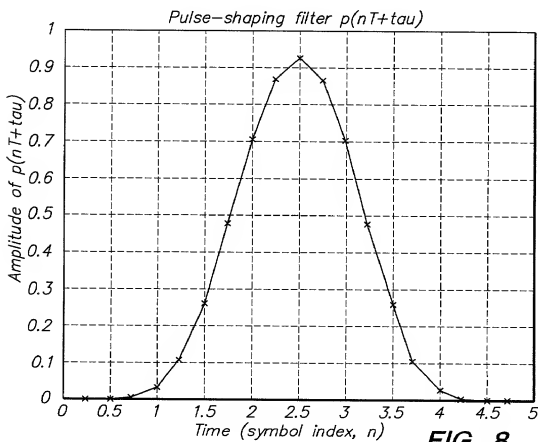


FIG. 6



**FIG. 7**



**FIG. 8**

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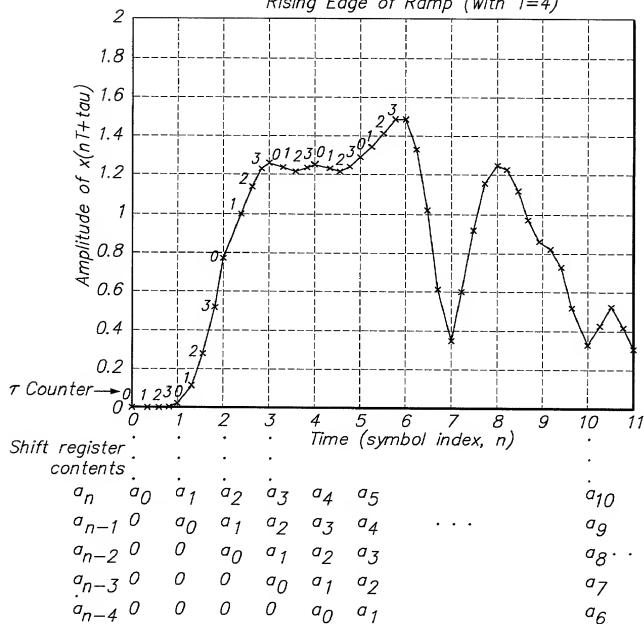
Rising Edge of Ramp (with  $T=4$ )

FIG. 9

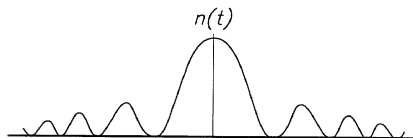
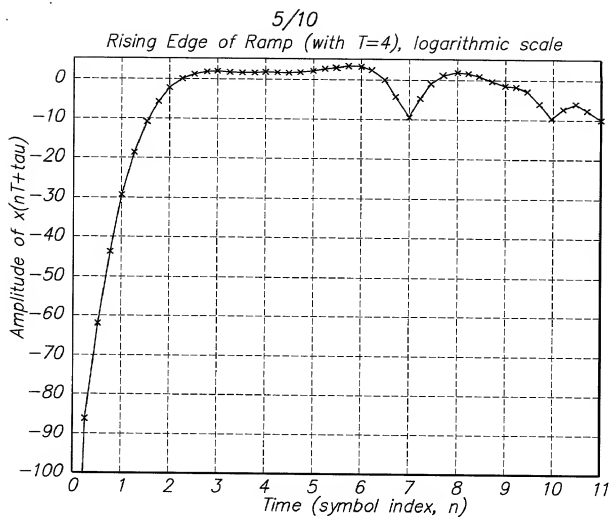
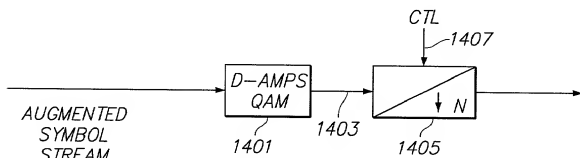


FIG. 13 (PRIOR ART)

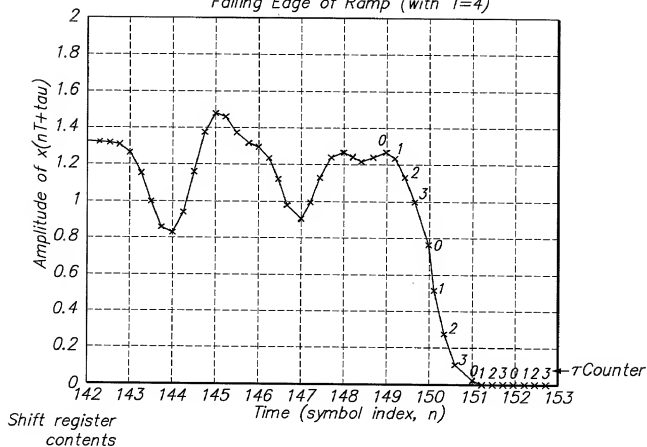


**FIG. 10**



**FIG. 14**

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Falling Edge of Ramp (with  $T=4$ )Shift register  
contents

$a_n$	$a_{142}$	...	$a_{146}$	$a_{147}$	0	0	0	0	0
$a_{n-1}$	$a_{141}$	...	$a_{145}$	$a_{146}$	$a_{147}$	0	0	0	0
$a_{n-2}$	$a_{140}$		$a_{144}$	$a_{145}$	$a_{146}$	$a_{147}$	0	0	0
$a_{n-3}$	$a_{139}$		$a_{143}$	$a_{144}$	$a_{145}$	$a_{146}$	$a_{147}$	0	0
$a_{n-4}$	$a_{138}$	...	$a_{142}$	$a_{143}$	$a_{144}$	$a_{145}$	$a_{146}$	$a_{147}$	0

FIG. 11

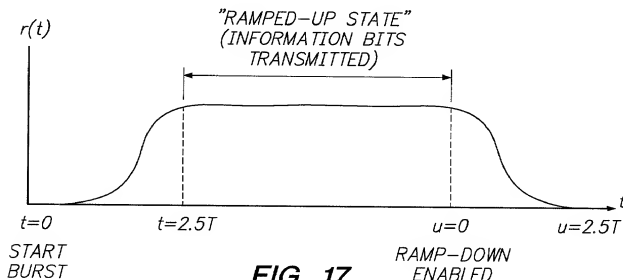


FIG. 17

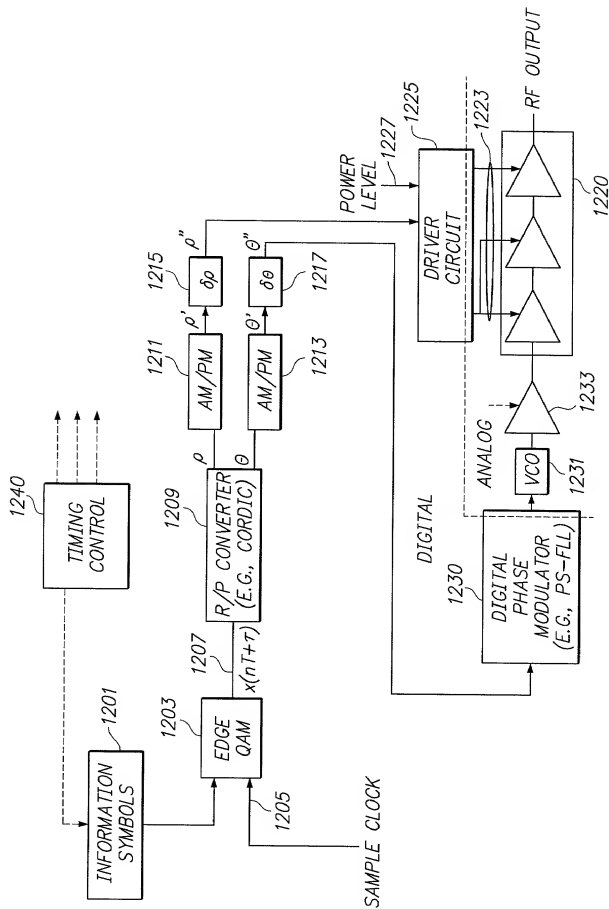


FIG. 12

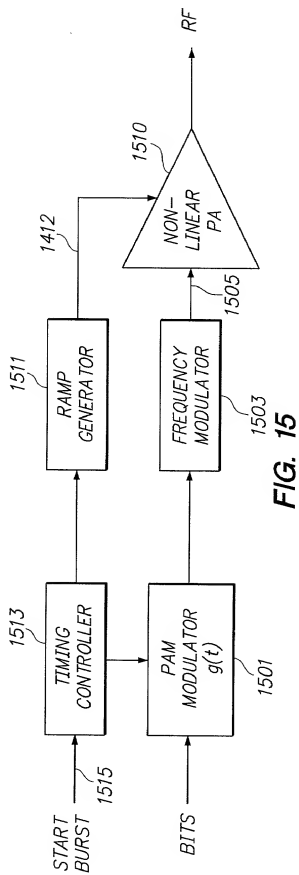


FIG. 15

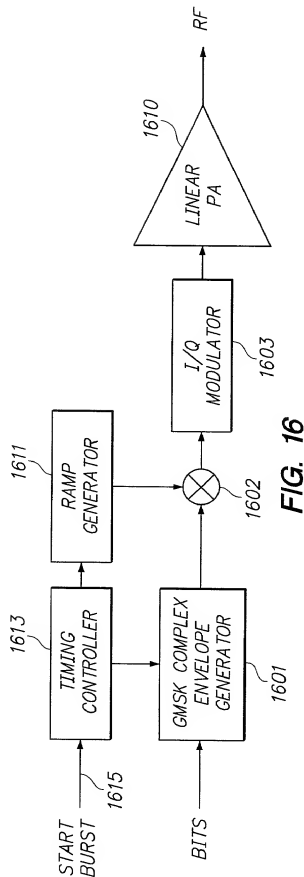


FIG. 16



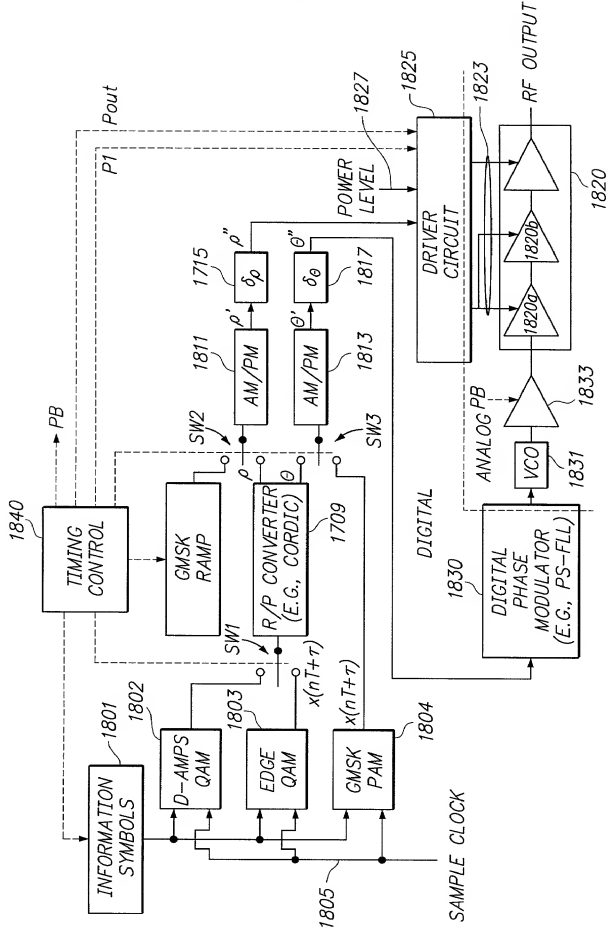


FIG. 18

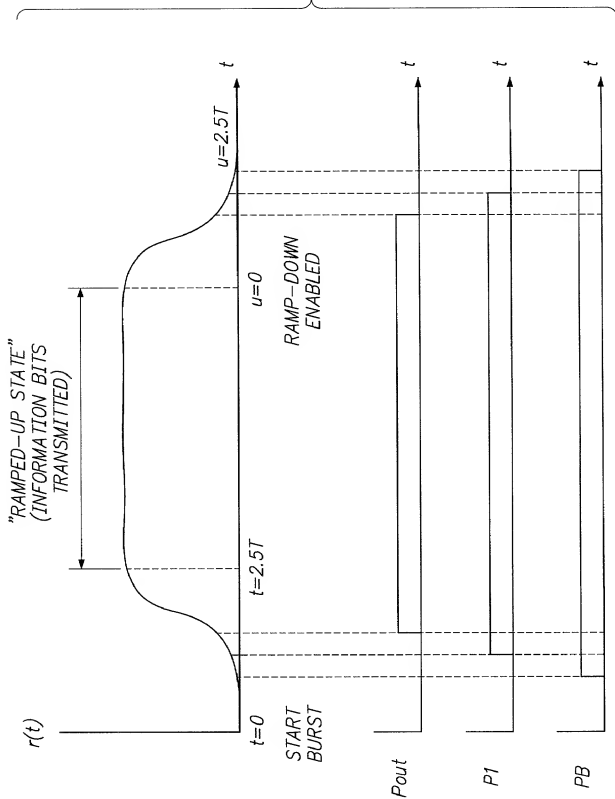


FIG. 19